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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application: Sang Hee KANG

] GRP ART UNIT: 2816

Serial No: 10/673,014

] Ex.: Ton, My Trang

Filed: SEPTEMBER 26, 2003

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For: SHARED DELAY CIRCUIT OF A SEMICONDUCTOR DEVICE

Certification under 37 C.F.R. §1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with The United States Postal Service with sufficient postage as first class mail in an envelope addressed to The Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 25, 2005.

W. William Park, Reg. No. 55,523

AMENDMENT UNDER 37 CFR 1.111

The Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Sir:

In response to the Office Action dated October 4, 2004, setting a 3-month shortened statutory period for a reply ending on January 4, 2005, Applicant submits the following responsive amendment in the above-identified application. This amendment is considered to place the application in better condition for allowance.

The Commissioner is authorized to charge required fee(s), if there are any, or credit any overpayment to Deposit Account No. 12-0400.

Amendments to the Specification begin on page 3 of this paper.

Amendment to the Drawings begin on page 4 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Amendments to the Claims are reflected in the listing of claims, which begins on page 5 of this paper.

Remarks/Arguments begin on page 8 of this paper.

An **Appendix** including amended drawing figures is attached following the last page of this paper.